



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 6414

SERIAL NUMBER 10/060,086	FILING DATE 01/31/2002 RULE	CLASS 712	GROUP ART UNIT 2154	ATTORNEY DOCKET NO. 2047.0600007
-----------------------------	---------------------------------------	--------------	------------------------	--

APPLICANTS

Sanjiv Garg, Fremont, CA;

Derek J. Lentz, Los Gatos, CA;

Le Trong Nguyen, Monte Sereno, CA; Sho Long Chen, Saratoga, CA;

** CONTINUING DATA *****

This application is a CON of 09/840,026 04/24/2001 ABN
which is a CON of 09/480,136 01/10/2000 PAT 6,249,856
which is a CON of 09/188,708 11/10/1998 PAT 6,044,449
which is a CON of 08/937,361 09/25/1997 PAT 5,838,986
which is a CON of 08/665,845 06/19/1996 PAT 5,682,546
which is a CON of 08/465,239 06/05/1995 PAT 5,560,035
which is a CON of 07/726,773 07/08/1991 PAT 5,493,687

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 02/25/2002

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 9	TOTAL CLAIMS 1	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged	Examiner's Signature	Initials	

ADDRESS

26111

STERNE, KESSLER, GOLDSTEIN & FOX PLLC

1100 NEW YORK AVENUE, N.W.

WASHINGTON, DC

20005

TITLE

RISC microprocessor architecture implementing multiple typed register sets

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
RECEIVED		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input type="checkbox"/> 1.18 Fees (Issue)